

only K transistors 18 are activated), the remaining column lines 16 are in a tri-state condition and are coupled to the nonselected capacitors 24 of the row. Therefore, charge sharing typically occurs between the capacitors 24 and the tri-stated column lines 16.

In the Claims:

Rewrite claims 11 and 14 as follows:

11. (Amended) A light modulator cell comprising:
a pixel cell;
a capacitor to maintain a terminal voltage of the pixel cell near a predetermined voltage;
a memory to store a digital indication of the predetermined voltage; and
a digital-to-analog converter to convert the digital indication into an analog voltage to update a charge on the capacitor during a refresh operation.

14. (Amended) The light modulator cell of claim 11, further comprising:
bit latches to latch the digital indication during the refresh operation.



CLAIM AMENDMENTS

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The claims have been amended as follows:

11. (Amended) A [An] light modulator cell comprising:
 - a pixel cell;
 - a capacitor to maintain a terminal voltage of the pixel cell near a predetermined voltage;
 - a memory to store a digital indication of the predetermined voltage; anda digital-to-analog converter to convert the digital indication into an analog voltage to update a charge on the capacitor during a refresh operation.

14. (Amended) The light modulator cell of claim 11 [10], further comprising:
bit latches to latch the digital indication during the refresh operation.